

INFORMATION DISCLOSURE CITATION
(Use several sheets if necessary)

Atty. Docket No. 04329.1952-01000				Serial No. Not yet assigned			
Applicant Yoshitaka TSUNASHIMA et al.							
Filing Date October 13, 2000				Group Not yet assigned			

JCS60 U.S. PTO
 09/588989
 10/17/00

U.S. PATENT DOCUMENTS							
Examiner Initial*	Document Number	Date	Name	Class	Sub Class	Filing Date If Appropriate	
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	5,254,489	10/19/93	Nakata	437	40		
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FOREIGN PATENT DOCUMENTS							
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	4-225531	08/14/92	Japan			No	
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
	Stanley WOLF, Silicon Processing for the Vlsi Era, Vol. I pp. 581 and Vol. II pp. 238-239. (1986, Ed.)						
Examiner				Date Considered April 6, 2007			
*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							
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